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- IEEE与IEEE Xplore简介
- 高效检索科研论文
- 搜索追踪学术热点
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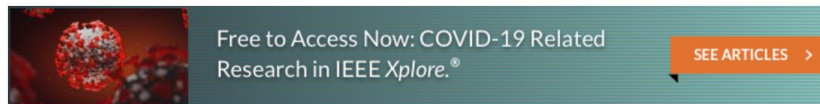
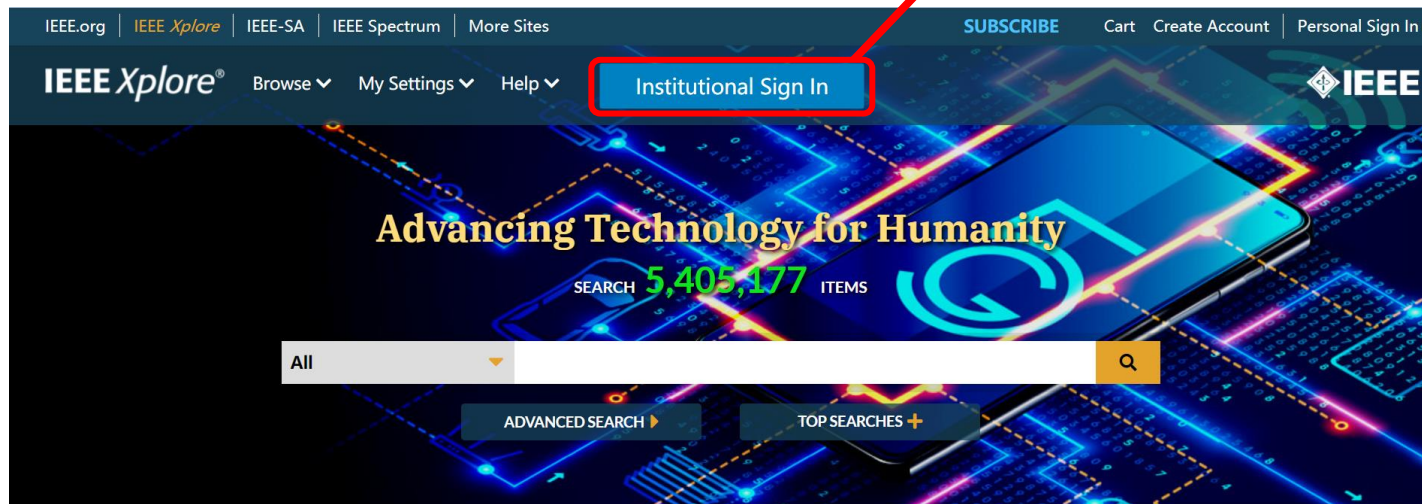
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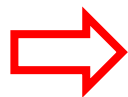
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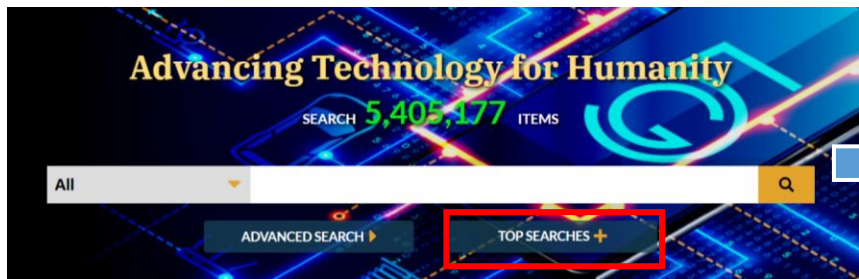


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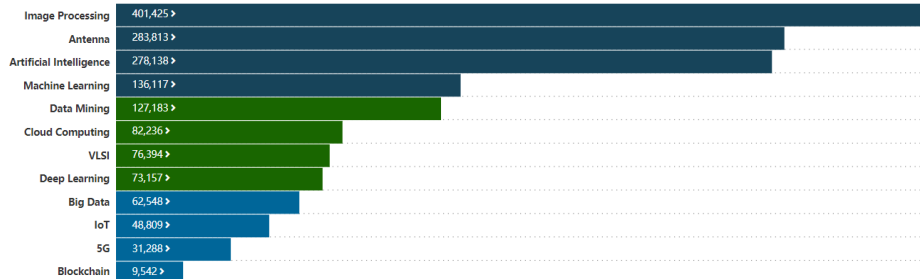
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Affiliation

Institute of Network Coding & Department of Information Engineering
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Hong Kong, China

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Biography

Raymond W. Yeung (Fellow, IEEE) was born in Hong Kong in June 1962. He received the B.S., M.Eng., and Ph.D. degrees in electrical engineering from Cornell University, Ithaca, NY, USA, in 1984, 1985, and 1988, respectively. He was on leave at the École Nationale Supérieure des Télécommunications, Paris, France, in Fall 1986. He was a Member of Technical Staff of AT&T Bell Laboratories from 1988 to 1991. Since 1991, he has been with The Chinese University of Hong Kong, where he is currently a Choh-Ming Li Professor of information engineering and the Co-Director of the Institute of Network Coding. He has held visiting positions at Cornell University, Nankai University, Bielefeld University, the University of Copenhagen, the Tokyo Institute of ... [Show More](#)

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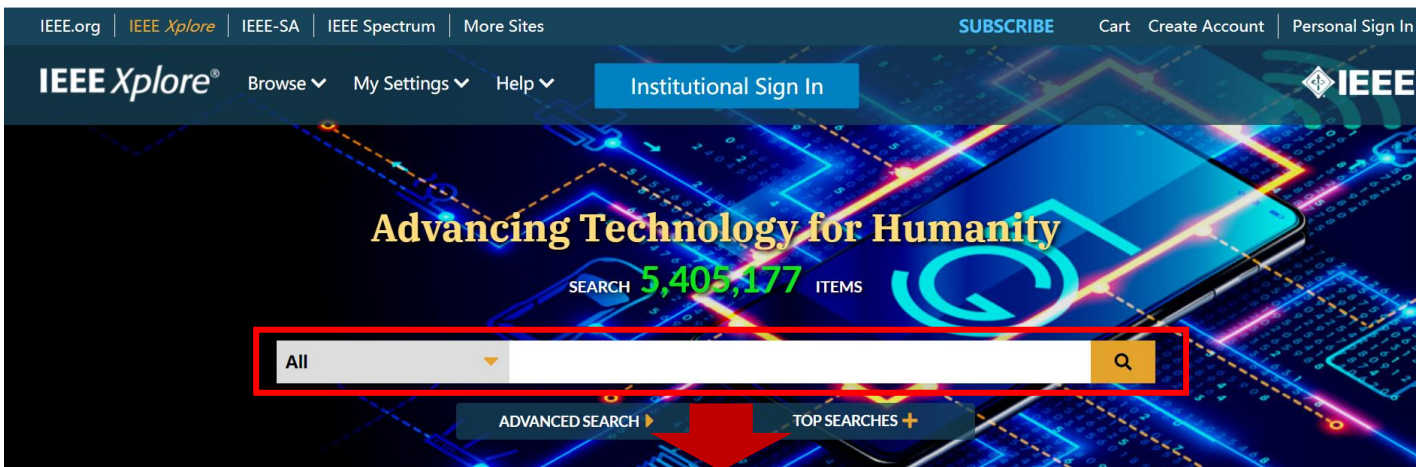


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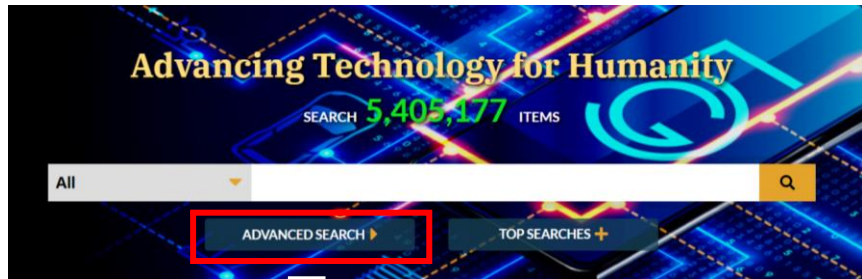
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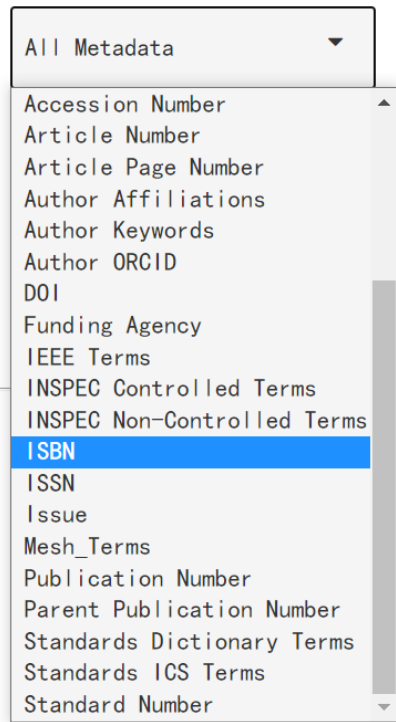
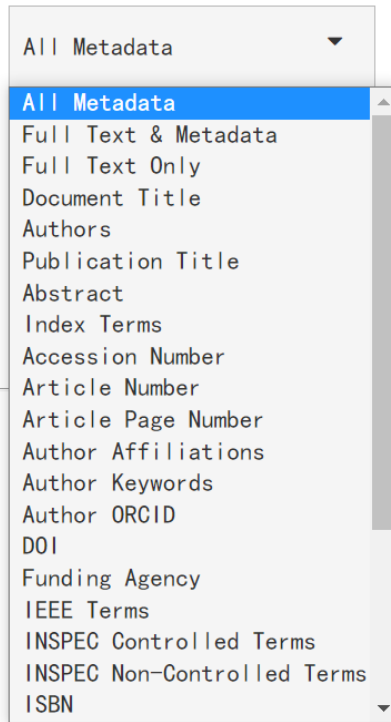
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Chiao-Shun Patrick Chuang; Kai-Yu Gary Chen; Yu-Ren Ryan Hung; Ta-Chuan Kuo; Cheng-Chin Tony Huang


2014 IEEE 26th International Symposium on Power Semiconductor Devices & IC's (ISPSD)

Year: 2014 | Conference Paper | Publisher: IEEE

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☐ Enhanced Electrical and Thermal Properties of Trench Metal-Oxide Semiconductor Field-Effect Transistor Built on Copper Substrate

Qi Wang; Ihsiu Ho; Minhua Li

IEEE Electron Device Letters

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☐ Fabrication and Characterization of a Metal Oxide Semiconductor Field Effect Transistor (MOSFET)-based Micro pH Sensor



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Hybrid MOSFET/driver for ultra-fast switching

T. Tang; C. Burkhart

IEEE Transactions on Dielectrics and Electrical Insulation

Year: 2009 | Volume: 16, Issue: 4 | Journal Article | Publisher: IEEE

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Abstract:

The ultra-fast switching of power MOSFETs, in about 1 ns, is very challenging due to the parasitic inductance that is intrinsic to commercial packages used for power MOSFET drivers. Parasitic gate and source inductance not only limit the voltage rise time but also the internal gate structure but can also cause the gate voltage to oscillate. This paper describes a hybrid approach that substantially reduces the parasitic inductance between the driver and MOSFET gate, as well as between the MOSFET source and its external connection. A flip-chip assembly is used to directly attach a die-form power MOSFET and driver on a PCB. The parasitic inductances are significantly reduced by eliminating bond wires and minimizing lead length. The experimental results demonstrate ultra-fast switching of the power MOSFET with excellent control of the gate-source voltage.

Published in: IEEE Transactions on Dielectrics and Electrical Insulation (Volume: 16, Issue: 4, August 2009)

Page(s): 967 - 970

INSPEC Accession Number: 10847239

9,5211841

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SECTION I. Introduction

Power MOSFETs have great potential as switches for high speed high voltage applications like pulsed power. the theoretical carrier transit time from drain to source is on the order of 200 ps in any cell of the silicon die [1]. Although the power MOSFET is

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Coreless printed circuit board (PCB) transformers for power MOSFET/IGBT gate drive circuits
IEEE Transactions on Power Electronics
Published: 1999

The reliability of high-lead solder joints in flip-chip devices
2014 15th International Conference on Electronic Packaging Technology
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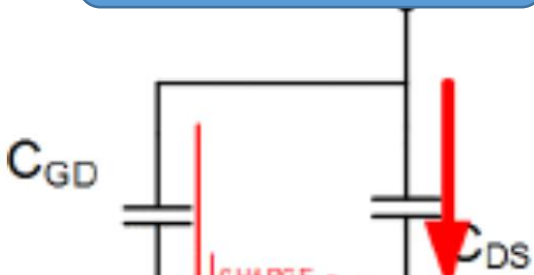
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2. "Application Advantages and Disadvantages of Modern Fast Switching MOSFETs in VRM", *PCIM Europe*, 2016.

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1. Can MOS

FET?

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of the semiconductor was proposed in [23], and was defined as:

$$\begin{aligned}\sum_{\text{SC}} &= \sum_{\text{chip}} + \sum_{\text{pack},x} \\ &= \left(\sum_n \sigma_{\text{chip},x(n)} A_{\text{chip},n} \right) + \sum_{\text{pack},x}\end{aligned}\quad (1)$$

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dv/dt Immunization limit of LV MOSFET in cascode GaN FET and dv/dt safe chart for MOSFETs

Publisher: IEEE

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Z. Chen; Jaume Roig Guitart All Authors

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1. R. Mitova and R. Ghosh, "Investigations of 600V GaN HEMT and GaN diode for the power converter applications", *IEEE Trans. Power Electron.*, vol. 29, no. 5, pp. 2441-2452, May 2014.
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2. "Application Advantages and Disadvantages of Modern Fast Switching MOSFETs in VRM", *PCIM Europe*, 2016.
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3. Christian R. Müller and Stefan Buschhorn, "Impact of module parasitics on the performance of fast switching devices", *PCIM Europe*, 2014.
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4. Alan Elbanhawy, MOSFET Susceptibility to Cross Conduction, Power Electronics Technology, April 2005.
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5. Alan Elbanhawy, AN-7019 Limiting Cross-Conduction Current in Synchronous Buck Converter Designs, Fairchild Semiconductor.
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6. Toni López I, Reinhold Elferich I and Eduard Alarcón, *Voltage Regulators for Next Generation*

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Requirements for low intermodulation distortion in GaN-Al/sub x/Ga/sub 1-x/N high electron mobility transistors: a model assessment
IEEE Transactions on
Published: 2002

Quantitative Discussi
Terahertz Waves of D
MOSFET and High-F

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1. Masahiro Koyama, Kentaro Ikeda, Kazuto Takao, "Novel cascode GaN module integrated a single gate driver IC with high switching speed controllability", *Power Electronics and Applications (EPE'18 ECCE Europe) 2018 20th European Conference on*, pp. P.1-P.8, 2018.
[Show Article](#) [Full Text: PDF \(2528KB\)](#) [Google Scholar](#)
2. Tianhua Zhu, Fang Zhuo, Feng Wang, Hailin Wang, Xiaoping Sun, Shuhuai Shi, Baohui Ma, "Quantitative Analysis and Suppression Strategies of Dv/dt Induced Turn-on of Cascode GaN FETs in Half-bridge Circuits", *Wide Bandgap Power Devices and Applications in Asia (WIPDA Asia) 2018 1st Workshop on*, pp. 130-134, 2018.
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3. Tianhua Zhu, Fang Zhuo, Fangzhou Zhao, Feng Wang, Tong Zhao, "Quantitative Model-Based False Turn-on Evaluation and Suppression for Cascode GaN Devices in Half-Bridge Applications", *Power Electronics IEEE Transactions on*, vol. 34, no. 10, pp. 10166-10179, 2019.
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4. Jian Chen, Xiong Du, Quanming Luo, Xinyue Zhang, Pengju Sun, Lin Zhou, "A Review of Switching Oscillations of Wide Bandgap Semiconductor Devices", *Power Electronics IEEE Transactions on*, vol. 35, no. 12, pp. 12182-12199, 2020.

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Analytical Modeling and Experimental Validation of Threshold Voltage in BSIM6 MOSFET Model

Publisher: IEEE

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Harshit Agarwal ; Chetan Gu

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III. Threshold Voltage
Model

IV. Simulation Results

V. Conclusion

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Chenming Hu

Department of Electrical Engineering and Computer
Science, University of California, Berkeley, Berkeley, CA,
USA

了解作者详情

Chenming Hu (F' 03) is the TSMC Distinguished Professor Emeritus of University of California Berkeley, Berkeley, CA, USA. He is a Former Chief Technology Officer of TSMC. He is a Board Director of SanDisk Inc., and of the non-profit Friends of Children with Special Needs. He is well known for his work on the 3-D transistor, FinFET, which can be scaled to single digit nanometers. He has developed widely used IC reliability models and led the research of BSIM—the first industry—standard SPICE model used by most IC companies to design CMOS products since 1996. He was a recipient of the IEEE Andrew Grove Award, the Solid State Circuits Award and Nishizawa Medal, the Kaufman Award of the EDA industry, the University Research Award of the U.S. Semiconductor Industry Association, and the UC Berkeley's Highest Honor for teaching—the Berkeley Distinguished Teaching Award.

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Understanding quasi-ballistic transport
in nano-MOSFETs: part II-Technology
scaling along the ITRS
IEEE Transactions on Electron Devices
Published: 2005

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anagerGroupState: f
var doChangeState
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Biography
Chenming Hu (LF' 03) is currently a Professor of Electrical Engineering and Computer Science at the University of California at Berkeley, Berkeley, CA, USA, and Friends of the Earth International. He is also the co-founder and CEO of his involvement in FinFET—the industry standard transistor technology for sub-100 nm products. (Based on document)

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Publications by Year



Co-Authors:
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Conference Location 

☐ **A Voting-Mechanism based Ensemble Framework for Constraint Handling Techniques** 

Guohua Wu; Xupeng Wen; Ling Wang; Witold Pedrycz; P. N. Suganthan


IEEE Transactions on Evolutionary Computation


Year: 2021 | Early Access Article | Publisher: IEEE


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Media 





Description 


This is the supplementary file of the article “A Voting-Mechanism based Ensemble Framework for Constraint Handling Techniques” published in IEEE Transactions on Evolutionary Computation. This file contains two parts. One part includes the details of the 57 real-world constrained optimization problems, which are used in Section IV in the manuscript. Another part is the experimental results, including the best/mean/median values of the ten comparison algorithms on the 57 real-world constrained optimization problems, as the supplementary data of Table I and Table II in the manuscript.


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Abdulhadi Shoufan
2020 IEEE International Symposium on Circuits and Systems (ISCAS)
Year: 2020 | Conference Paper | Publisher: IEEE
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会议视频

Open Source RFNoC-Based Testbed for Millimeter-Wave Experimentation using USRP Software Defined Radios

2020 IEEE International Symposium on Circuits and Systems Virtual, October 10-21, 2020

Open Source RFNoC-Based Testbed for Millimeter-Wave Experimentation Using USRP Software Defined Radios

Adriana Moreno ^{*}, Jesús Omar Lacruz ^{*}, Joerg Widmer ^{*}

^{*} IMDEA Networks Institute, ^o Universidad Carlos III de Madrid, Spain

2020 IEEE International Symposium on Circuits and Systems Virtual, October 10-21, 2020

0:00 5:18 1x

Transcript

Open Source RFNoC-Based Testbed for Millimeter-Wave using USRP Software Defined Radios

[00:03] JESUS OMAR LACRUZ Hello. I am Jesus Omar Lacruz, I am from IMDEA Networks Institute, Madrid, Spain. I will be in charge of presenting the 2020 International Symposium On Circuits and Systems entitled "Open source RFNoC-based testbed for millimeter-wave experimentation using USRP software defined radios." This technology, millimeter-wave communication requires suitable platforms to speed up data collection and validation.

[00:38] JESUS OMAR LACRUZ If we list the [INAUDIBLE] flexibility, the configuration to different conditions, and of course, affordability. We can find solutions for millimeter-wave testbed with different characteristics ideal for different scenarios. Some works use commercial devices as research platforms.

[01:06] JESUS OMAR LACRUZ The main problem is the physical layer information. On the other hand, commercial prices that could be not affordable for all research groups that USRPs has proven efficacy in sub-6-gigahertz networks, millimeter-wave systems will bring the desired flexibility, a wide online open-source community.

[01:35] JESUS OMAR LACRUZ Besides enhancing its full RFNoC framework, [INAUDIBLE] the implementation of sub-6 blocks in the FPGA, which is very important to reduce latency in a hardware-in-the-loop manner. Keeping this in mind, we designed and implemented a millimeter-wave experiment using USRPs and 60-gigahertz transceivers. We take advantage of the RFNoC framework to implement the hardware processing of the preamble of IEEE 802.11ad compliant frames in real-time.

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Personalized Channel Recommendation Deep Learning From a Switch Sequence

Can Yang ; Sixuan Ren ; Yong Liu ; [Houwei Cao](#) ; Qihu Yuan ; Guoqiang Han

IEEE Access

Year: 2018 , Volume: 6

Page s: 50824 - 50838

IEEE Journals & Magazines

► Abstract

« html »

(11429 Kb)



Datasets

数据

CHANNELS SWITCH SEQUENCES OF 300 IPTV VIEWERS IN A MONTH

Citation Author(s): Sixuan Ren and Can Yang in South China University of Technology

Submitted by: Can Yang

Last updated: Thu, 11/08/2018 - 10:34

DOI: 10.21227/H2396N

Data Format: TXT

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► Communications

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► IPTV, Recommender System, Machine Learning

Abstract:

This dataset includes the Channels Switch Sequences of 300 IPTV viewers in Guangzhou, P.R. China, in August, 2014. There are 4 columns in the file, which represent viewer ID, the current channel number, the next channel number, the date of the month, respectively. The first column, the ID code of a viewer, ranks in descent with the times the viewer watched tv channels. The more times a viewer watches tv channels, the bigger the ID is. In a day, the rows are time series and generated step by step as the real watching tv behavior.

DATASET FILES

► IPTVChannelSwitchSequencesUsers300.txt (3.91 MB)

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Optimal microgrids placement in electric distribution systems using complex network framework
Mahmoud Saleh ; Yusef Esa ; Nwabueze Onuorah ; Ahmed A. Mohamed
2017 IEEE 6th International Conference on Renewable Energy Research and Applications (ICRERA)
Year: 2017
Page s: 1036 - 1040
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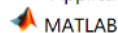
Code & Datasets

Code

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Code: Applications of Complex Network Analysis in Electric Power Systems



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3
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

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2014 IEEE 26th International Symposium on Power Semiconductor Devices & IC's (ISPSD)

Year: 2014 | Conference Paper | Publisher: IEEE


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Qi Wang; Ihsiu Ho; Minhua Li

IEEE Electron Device Letters

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Affiliation

State Key Laboratory of Media Convergence and Communication
Communication University of China
Beijing, China

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Biography

Zengrui Li (Member of IEEE) received the B.S. degree from the Beijing Broadcasting Institute, Beijing, in 1987, and the Ph.D. degree from Beijing Jiaotong University, in 2009, both in electrical engineering. From 2004 to 2005, he has studied with Yokohama National University, Japan. He is currently a Professor with the Communication University of China, Beijing. His research interests include the areas of finite-difference time-domain (FDTD) methods, electromagnetic scattering, metamaterials, and antennas. He is a Senior Member of the Chinese Institute of Electronics. (Based on document published on 25 January 2021).

追踪作者发文

on Polarization Interference

Yao Lu; Jianxun Su; Jinbo Liu; Qingxin Guo; Hongcheng Yin; Zengrui Li; Jintong Song

IEEE Transactions on Antennas and Propagation

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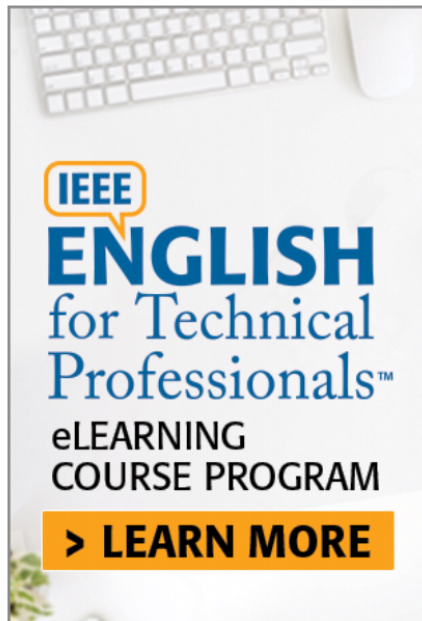
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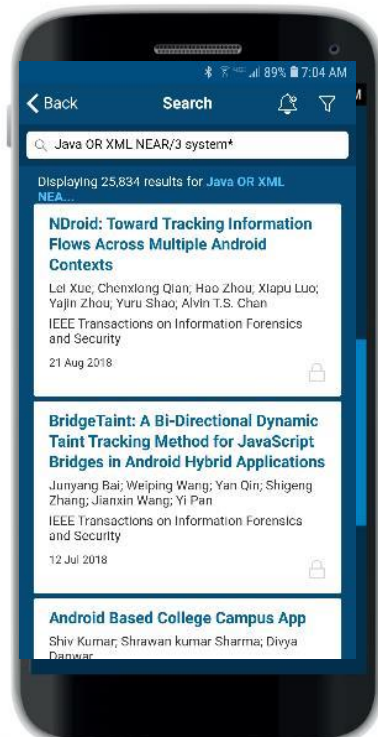
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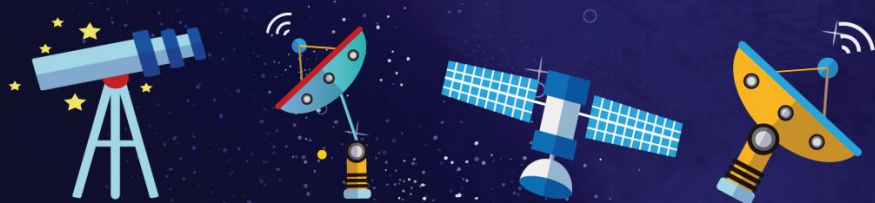
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IEEE Fellow 云论坛 ——半导体领域

技术进步与应用驱动成就IC五十年大发展

论坛介绍

本论坛以1971年Intel第一代CPU芯片4004出现为第一个十年的起点，回顾之后每一个十年，共五十年（1970-2020）的集成电路发展历程。这个发展步伐目前依然没有显著放慢。集成电路这半世纪无与伦比的快速发展依赖于两个基本动力：永无止境的技术创新与不断涌现的应用需求。

嘉宾介绍



特邀主讲嘉宾：余志平教授

清华大学微电子学研究所教授、博士生导师，IEEE Life Fellow。其专业领域是集成电路计算机辅助设计（ICCAD），主要从事半导体器件模拟与电路集约模型建模。近年来同时从事CMOS射频电路设计与微电子学研究。已发表学术论文近400篇，合著英、中文专著各一本。七五计划期间（1986-1990）参与组织的熊猫集成电路设计系统获1993年国家科技进步一等奖。



李菁

IEEE亚太区区域经理，负责为部分亚太地区包括中国大陆、香港及澳门客户提供科技创新解决方案。曾担任IEEE中国区资讯经理，负责大中华区的IEEE客户培训以及IEEE高校合作伙伴项目。曾就读于北京大学信息管理学院及美国雪城大学信息科学学院，在IEEE、ACM、AIS、ASEE等国际学术机构发表多篇论文及会议报告。

IEEE Fellow 云论坛 ——自动驾驶领域

无人车·平行驾驶·智能交通·智慧城市

论坛简介

随着物联网、云计算、大数据、人工智能、5G、区块链等新一代信息技术的快速发展，信息系统、物理系统、人类社会进一步相互融合，形成了工程复杂性与社会复杂性相耦合的复杂系统，即信息物理社会系统（Cyber-Physical-Social Systems, CPSS）。IEEE作为全球最大的科技学会，在物联网、智能驾驶、大数据等相关领域具有技术权威性。

此次论坛特别邀请了IEEE Fellow，智能控制、智能机器人、无人驾驶、智能交通等领域早期开拓者之一的王飞跃教授，为大家带来“基于CPSS的交通5.0：无人车、平行驾驶、智能交通、智慧城市”的主题报告。

特邀嘉宾：王飞跃教授、IEEE Fellow

主要研究复杂系统、智能控制、智能交通、智能机器人、无人驾驶、平行智能、社会计算、知识自动化等领域。现为中国科学院自动化研究所复杂系统管理与控制国家重点实验室主任，《智能科学与技术学报》主编，曾任IEEE多个期刊主编，现为IEEE Trans. on Intelligent Vehicles新任主编，欢迎大家投稿！



嘉宾：何丹丹

IEEE中国客户与资讯经理，负责中国地区（包括台湾香港）IEEE客户培训工作。从事科技信息服务工作多年，为高校、科研院所、企业及政府机构提供IEEE研发资源的培训及相关科研支持服务。



IEEE PELS 网络技术论坛

面向新一代电网的电力电子技术和装备新进展

论坛介绍

本次论坛主题聚焦新一代电网的电力电子技术和装备新进展，就电力电子系统、能源互联网、无线电能传输等技术方向的研究及进展进行探讨。

论坛嘉宾



赵争鸣教授

清华大学电机工程与应用电子技术系教授、IEEE Fellow、IET Fellow



姬世奇

清华大学电机系
助理教授，特别研究员



陈凯楠

清华大学能源互联网研究院
无线电能传输研究室主任



施博辰

清华大学电机系
博士研究生



虞竹理

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主题内容	直播日期	直播时间
主题一（a）：IEEE Xplore高效检索、文献调研及热点追踪	9月16日	19:30-20:30
主题一（b）：IEEE Xplore数据库高阶检索	9月23日	19:30-20:30
主题二：IEEE Xplore新动态：推动研究与创新	10月14日	19:30-20:30
主题三：IEEE期刊会议投稿流程与注意事项	10月14日	19:30-20:30
主题四：IEEE科技论文英文写作剖析	10月21日	19:30-20:30
主题五：以IEEE为例谈专业学协会对理工科学生职业发展的影响	11月05日	19:30-20:30
主题六：IEEE标准简介	11月12日	19:30-20:30

有问题请随时联系我们!

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